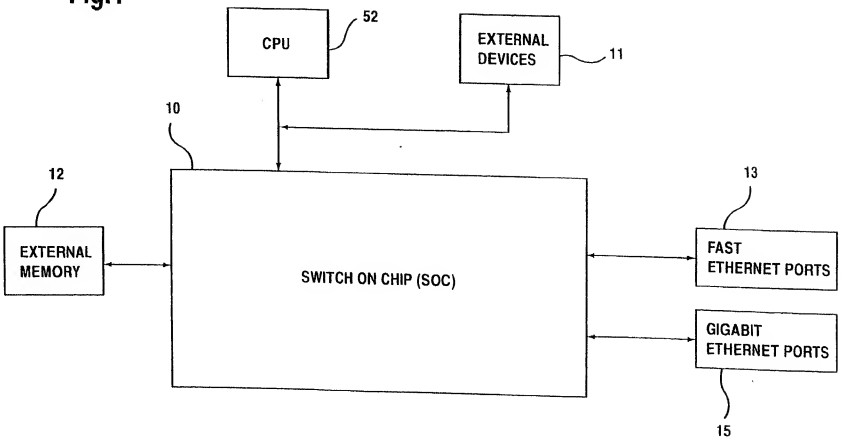
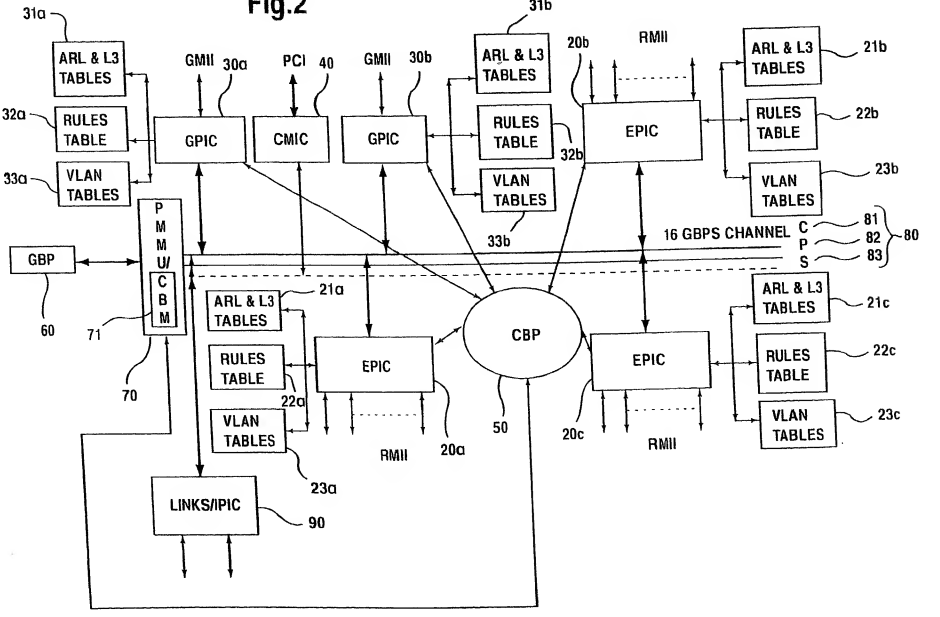
EP 2 672 680 A1

Fig.1



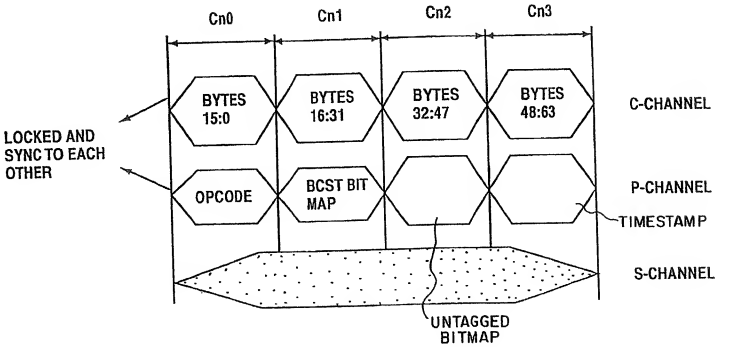
3827339 A6SE690T

Fig.2



EP 0 762 689 A1

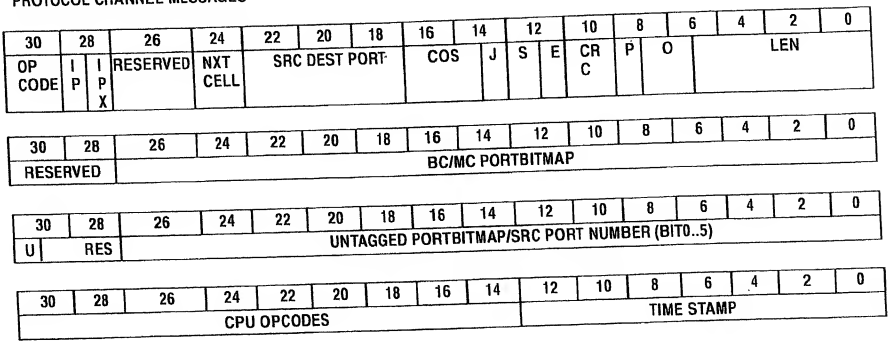
Fig.3



5,282,201 1/25/94

Fig. 4

PROTOCOL CHANNEL MESSAGES



3824230 1662800T

Fig. 5

SIDE BAND CHANNEL MESSAGES

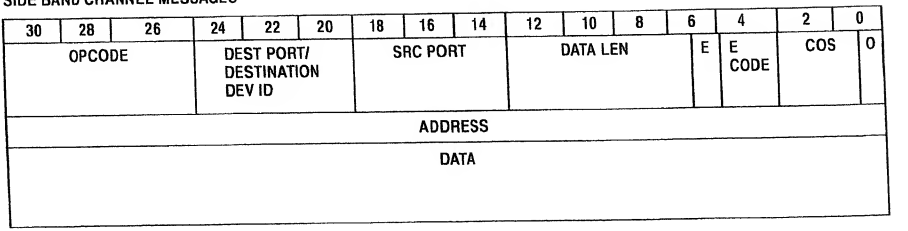


Fig. 6

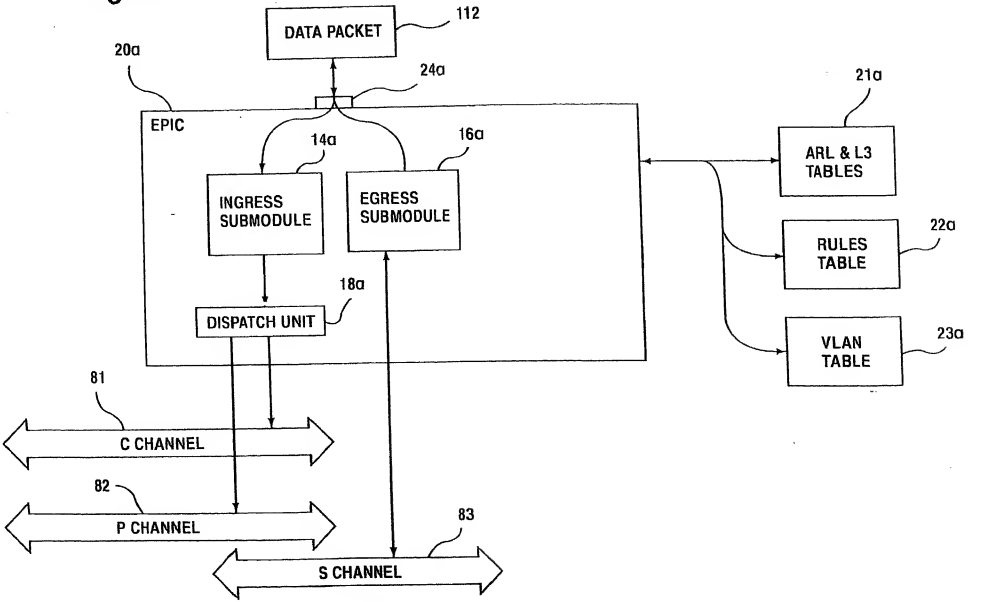

DATA PACKET
112
20a
24a
21a
EPIC
14a
16a
ARL & L3 TABLES
INGRESS SUBMODULE
EGRESS SUBMODULE
RULES TABLE
22a
18a
DISPATCH UNIT
VLAN TABLE
23a
81
C CHANNEL
82
P CHANNEL
83
S CHANNEL

Fig. 7

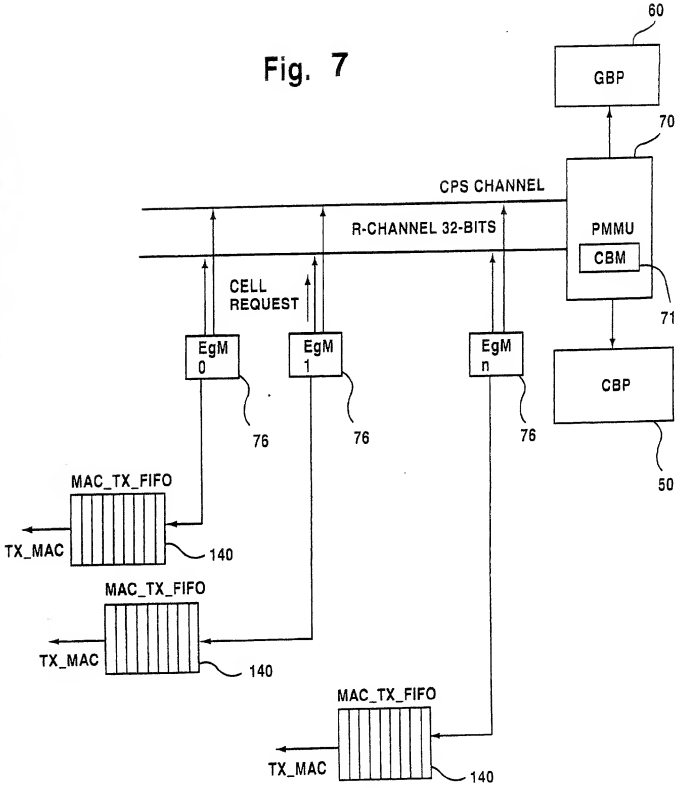
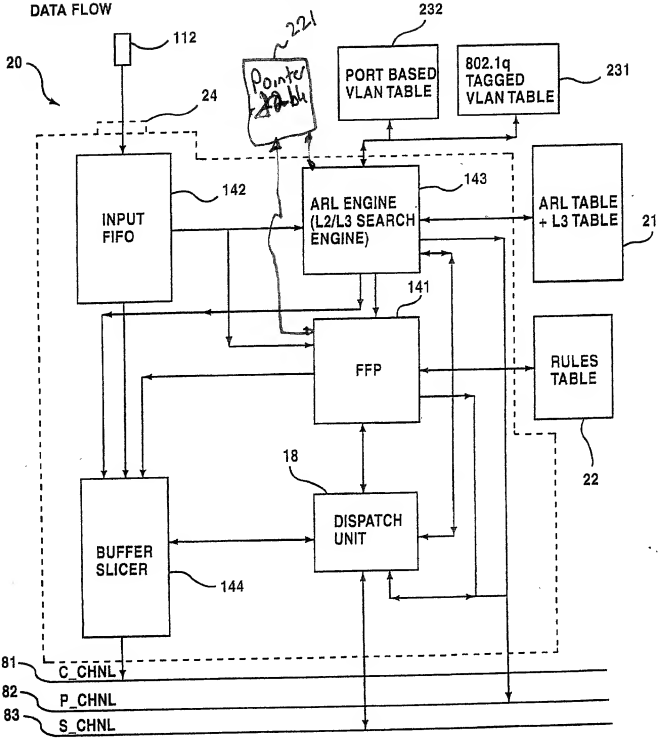

60
GBP
70
CPS CHANNEL
R-CHANNEL 32-BITS
PMMU
CBM
71
CELL
REQUEST
EgM
0
EgM
1
EgM
n
CBP
76
76
76
50
MAC_TX_FIFO
TX_MAC
140
MAC_TX_FIFO
TX_MAC
140
MAC_TX_FIFO
TX_MAC
140

Fig. 8


DATA FLOW
20
112
24
221
Pointer
232
PORT BASED VLAN TABLE
802.1q TAGGED VLAN TABLE
231
INPUT FIFO
142
ARL ENGINE (L2/L3 SEARCH ENGINE)
143
ARL TABLE + L3 TABLE
21
141
FFP
RULES TABLE
22
18
DISPATCH UNIT
BUFFER SLICER
144
81
C_CHNL
82
P_CHNL
83
S_CHNL

20230104656800T

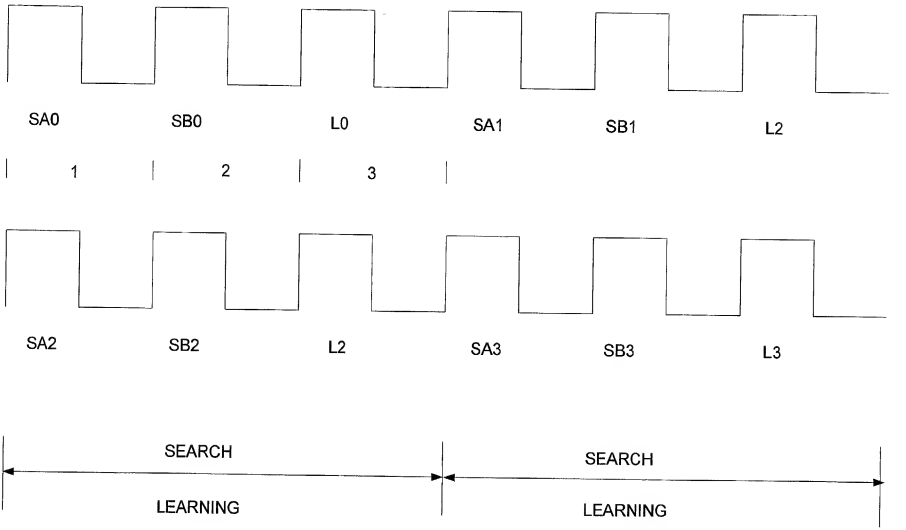


FIG 9

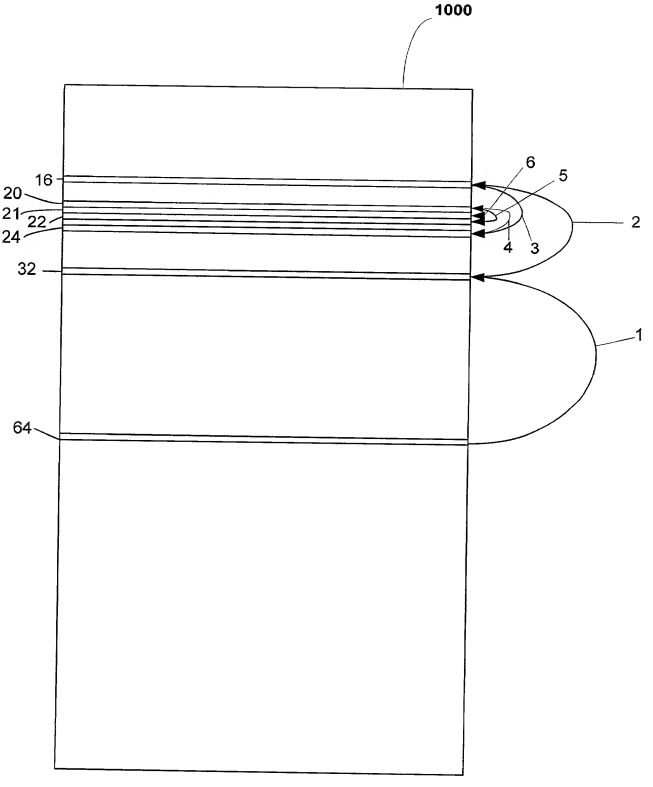


Figure 10

20220166680

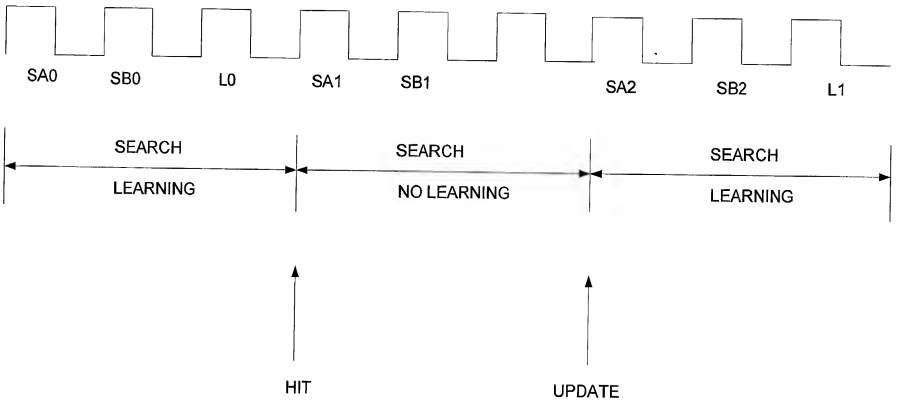


FIG 11

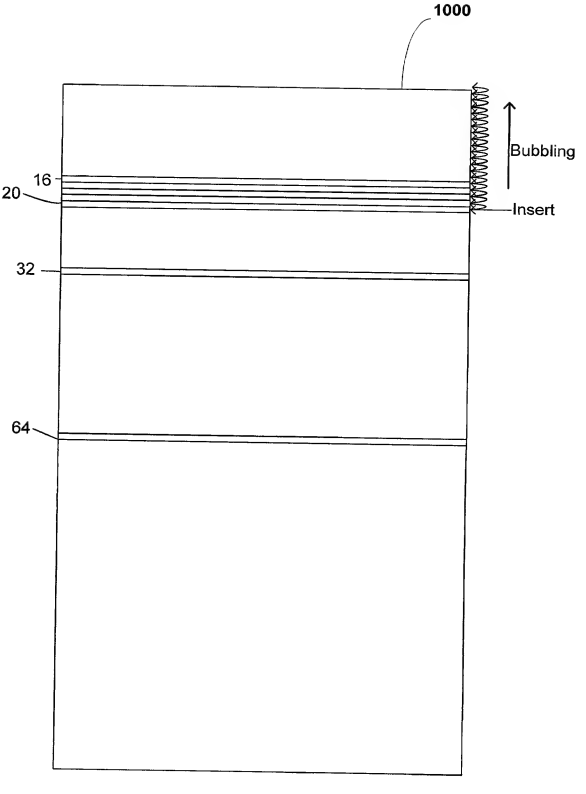


Figure 12

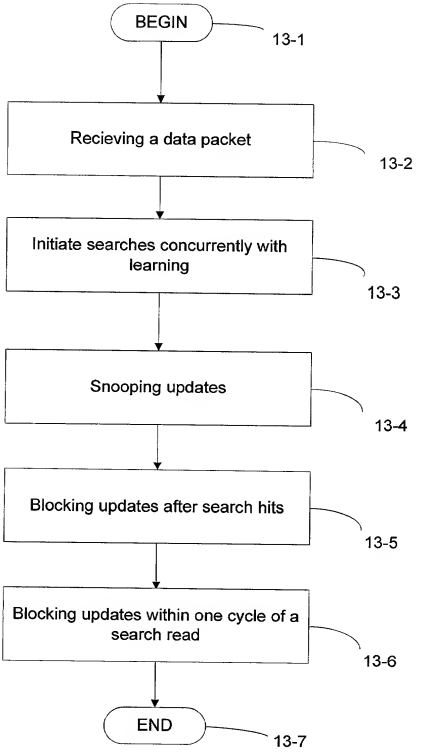

BEGIN
13-1
Recieving a data packet
13-2
Initiate searches concurrently with learning
13-3
Snooping updates
13-4
Blocking updates after search hits
13-5
Blocking updates within one cycle of a search read
13-6
END
13-7

FIG. 13